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Applicant Yasuhiro YAMAJI							
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U.S. PATENT DOCUMENTS							
Examiner Initial*		Document Number	Date	Name	Class	Sub Class	Filing Date If Appropriate
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Sub Class	Translation Yes or No
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
		Mitsuru OIDA et al.; "SEMICONDUCTOR DEVICE INCLUDING INTEGRATED CIRCUITS ON A POLYHEDRAL SEMICONDUCTOR"; US Patent Application Serial No. 09/499,692, f iled February 8, 2000					
		Kevin Chen et al.; "ULTRA-THIN ELECTRONIC DEVICE PACKAG E"; 1999 Electronic Components and Technology Conference; IEEE					
Examiner 				Date Considered 5/30/02			
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